

## REMARKS

In the final office action mailed on July 6, 2006, claims 1, 3 and 7 - 10 were rejected under 35 U.S.C. §102(b) over Japanese Patent Document No. JP 06-177,680 (to Yukio et al.), claims 15, 20 and 21 were rejected under §102(b) over U.S. Patent No. 5,054,116 (to Davidson), claim 2 was rejected under 35 U.S.C. §103(a) over Yukio et al. in view of U.S. Patent No. 6,566,944 (to Pehke et al.), claim 4 was rejected under §103(a) over Yukio et al. in view of U.S. Patent No. 6,617,930 (to Nitta), claim 5 was rejected under §103(a) over Yukio et al. in view of U.S. Patent No. 6,795,712 (to Vakilian et al.), claim 6 was rejected under §103(a) over Yukio et al. in view of U.S. Patent No. 6,144,860 (to Komatsu), claim 11 was rejected under §103(a) over Yukio et al. in view of U.S. Patent Publication No. 2004/0166802 (to McKay Sr., et al.), claim 16 was rejected under §103(a) over Davidson in view of U.S. Patent No. 5,656,929 (to Humpherys), claim 17 was rejected under 35 U.S.C. §103(a) over Davidson in view of U.S. Patent Publication No. 2003/0092407 (to Nitta), claim 18 was rejected under §103(a) over Davidson in view of Vakilian et al., claim 19 was rejected under §103(a) over Davidson in view of Yukio et al., and claims 12 - 14 were allowed.

In short, independent claim 1 was rejected as being anticipated by Yukio et al., and independent claim 15 was rejected as being anticipated by Davidson.

Claim 1 is amended to state that the subtraction means is for subtracting *both* a feedback signal *and* an output of said track and hold circuit *from* the measured reference power signal to provide a power control signal that is coupled to the power control input, such that said power control signal is *responsive* to the feedback signal, the output of the track and hold circuit, and the measured reference power signal. In view of the reasons stated for allowance of claim 12 in the office action, claim 1 is submitted to be in condition for allowance.

Independent claim 15 is amended to state that the combiner means is for providing a corrective signal to the power amplifier responsive to the reference signal, an output signal from said track and hold circuit, and the feedback signal by subtracting *both* the feedback signal and the output of said track and hold circuit *from* the reference signal to provide the corrective signal.

Each of claims 1 and 15, therefore, is submitted to be in condition for allowance. Each of dependent claims 2 - 11 depends directly or indirectly from claim 1 and further limits the subject matter of claim 1. Each of dependent claims 16 - 21 depends directly or indirectly from claim 15 and further limits the subject matter of claim 15. Each of claims 1 - 11 and 15 - 21, in addition to claims 12 - 14, is therefore submitted to be in condition for allowance.

Each of claims 1 - 21, therefore, is in condition for allowance. Favorable action consistent with the above respectfully requested.

Respectfully submitted,



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